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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Keith E. FOGEL et al. CONFIRM. NO.: 6557
 SERIAL NO.: 10/815,103 ART UNIT: 2826
 FILED: 03/31/2004 EXAMINER: L. ANDUJAR
 TITLE: INTERCONNECTIONS FOR FLIP-CHIP USING LEAD-FREE
 SOLDERS AND HAVING REACTION BARRIER LAYERS

ATTORNEY DOCKET NO.: YOR920030190US1

Mail Stop A F
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT, CERTIFICATION
UNDER 37 C.F.R. 1.97(e)(1) AND FEE UNDER 37 C.F.R. 1.17(p)**

In accordance with the duty of disclosure, applicants call to the attention of the Examiner the document cited on the attached form PTO-1449 (Substitute). It is respectfully requested that the Examiner consider this document, and make it of record.

I hereby certify that each item of information cited herein was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this paper.

Please charge the fee of \$180 required by 37 C.F.R. 1.17(p) to deposit account 50-0510. A duplicate of this page is enclosed.

Respectfully submitted,

David Aker
 David Aker, Reg. No. 29,277
 23 Southern Road
 Hartsdale, NY 10530
 Tel. & Fax 914 674-1094

2/8/2008

Date

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted on the date listed below to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date listed below.

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